Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2	(processing adj circuit) and (first adj (linear adj transform$)) and (second adj (linear adj transform$))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/11 12:31
L2	1	(processing adj circuit) and ((first adj (linear adj transform$)) same (second adj (linear adj transform$)) same combin$)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/11 12:31
S1	1708	716/1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/11 10:33
S2	1	(716/1).ccls. and (processing adj circuit) and (parallel adj processing$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/11 10:37
S3	10	("716"/$).ccls. and (processing adj circuit) and (parallel adj processing$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/11 10:39
S4	1225	(processing adj circuit) and (parallel adj processing$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/11 10:42
S5	983	(processing adj circuit) and (first adj processing$2) and (second adj processing$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/11 10:43
S6	14	(processing adj circuit) and (first adj processing$2) and (second adj processing$2) and (linear adj transform$)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/11 12:19